

FIGURE 1

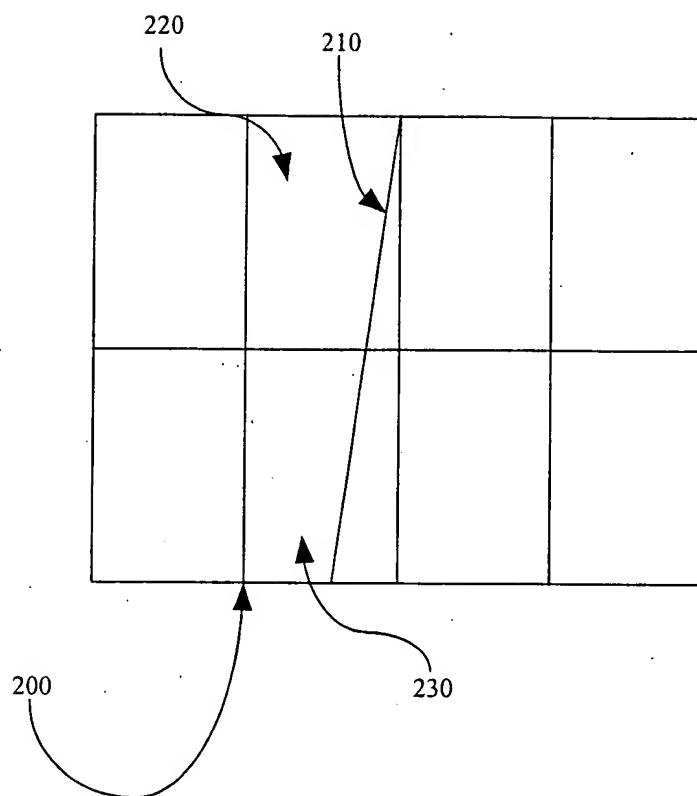
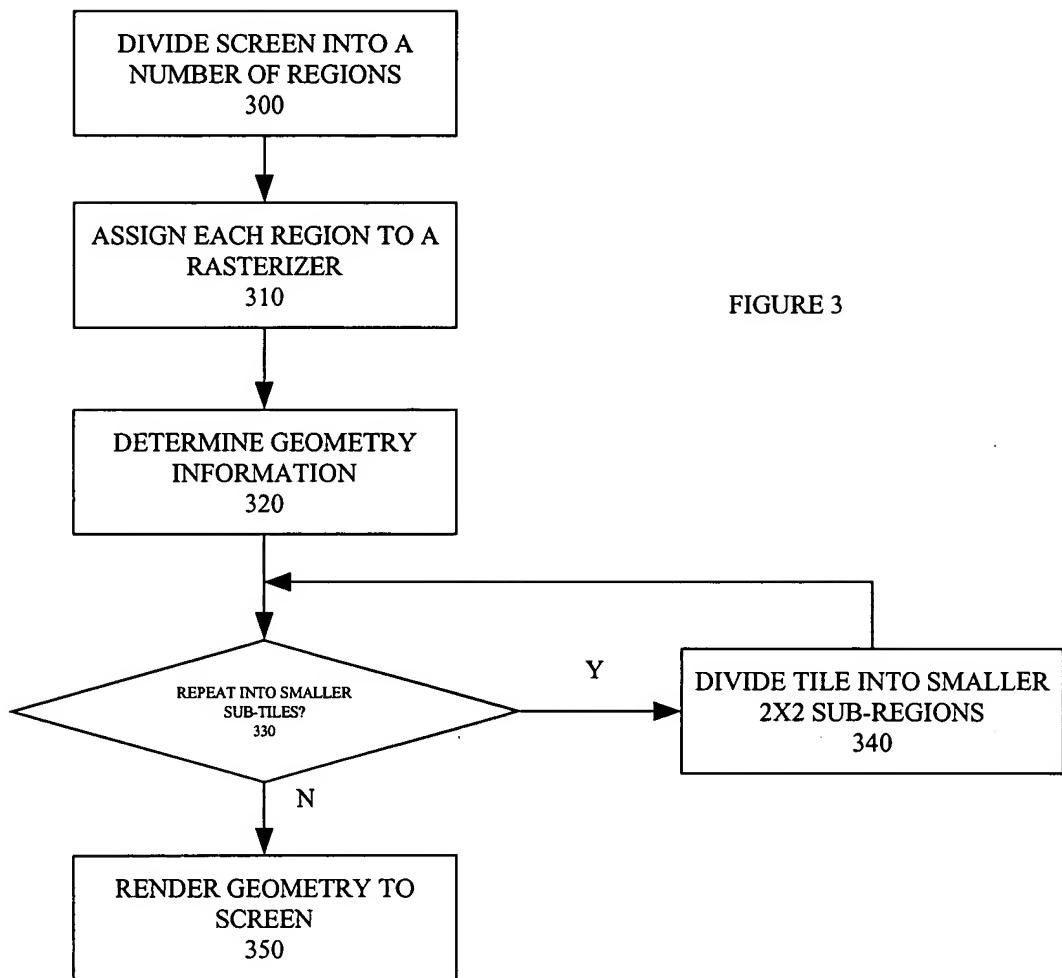
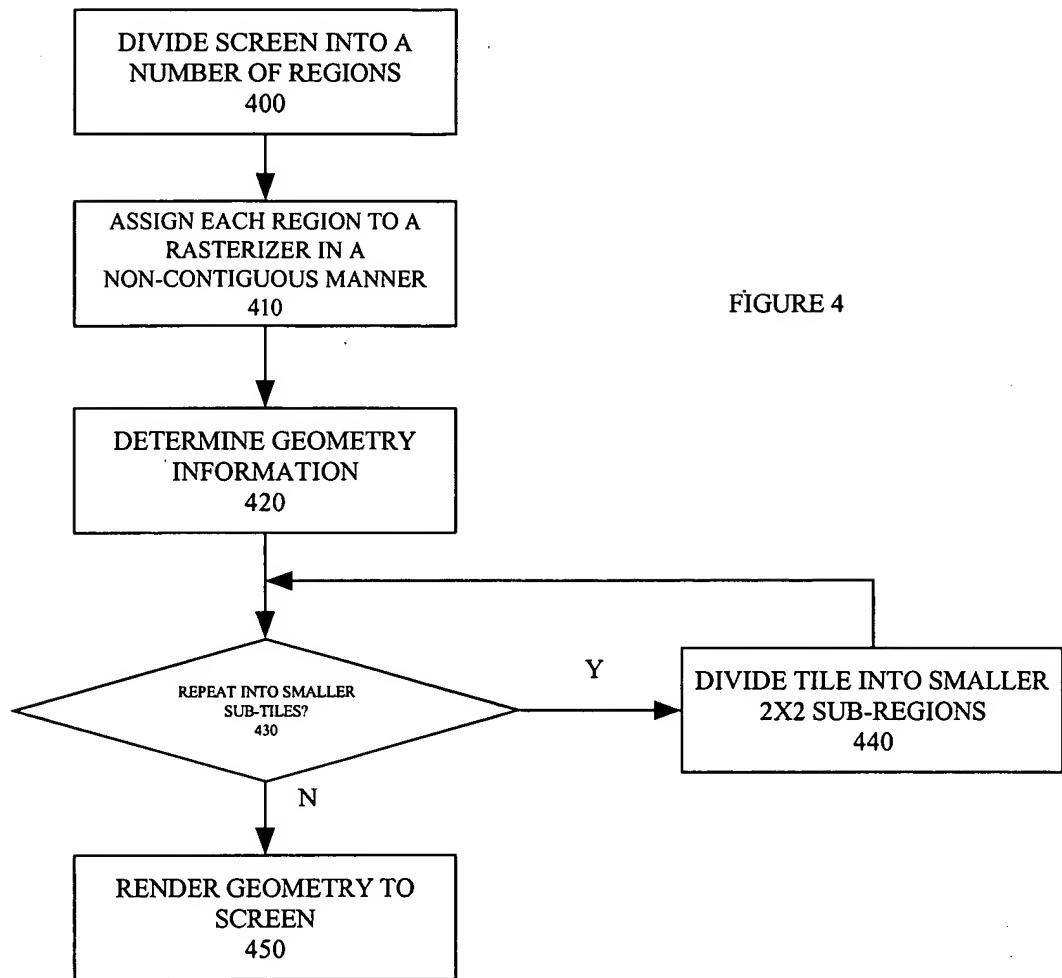


FIGURE 2





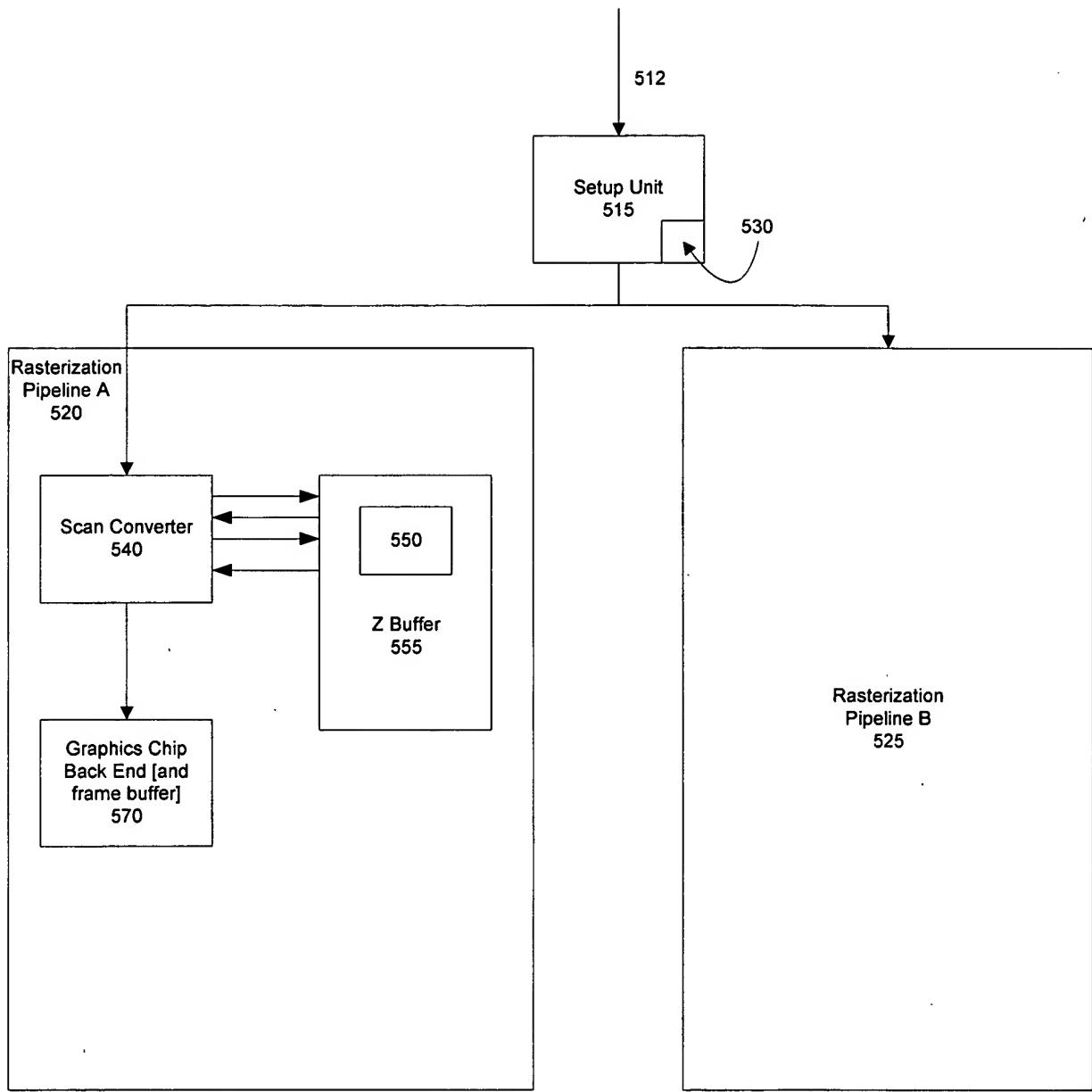


FIGURE 5

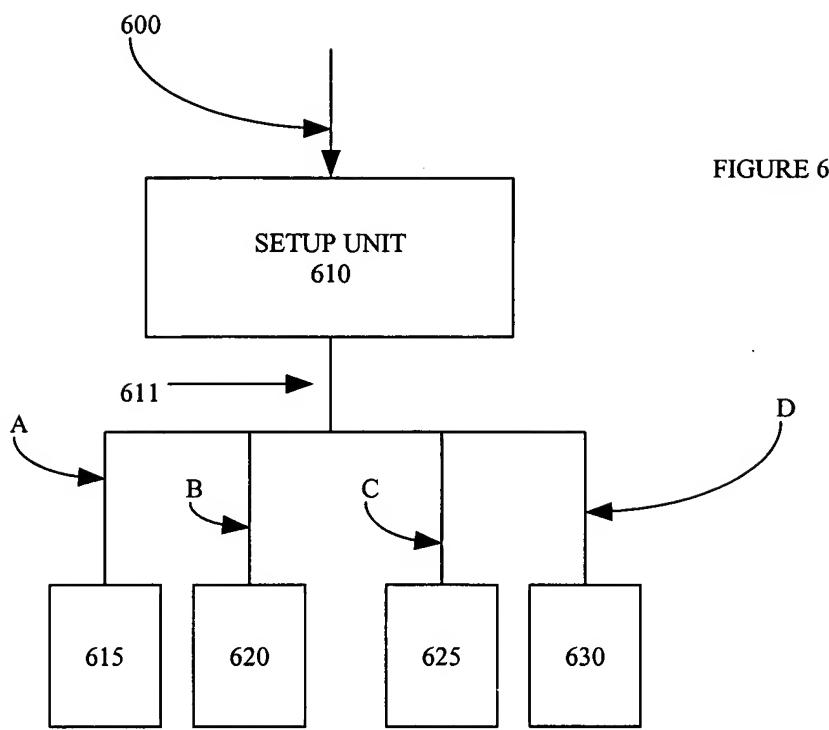


Figure 7

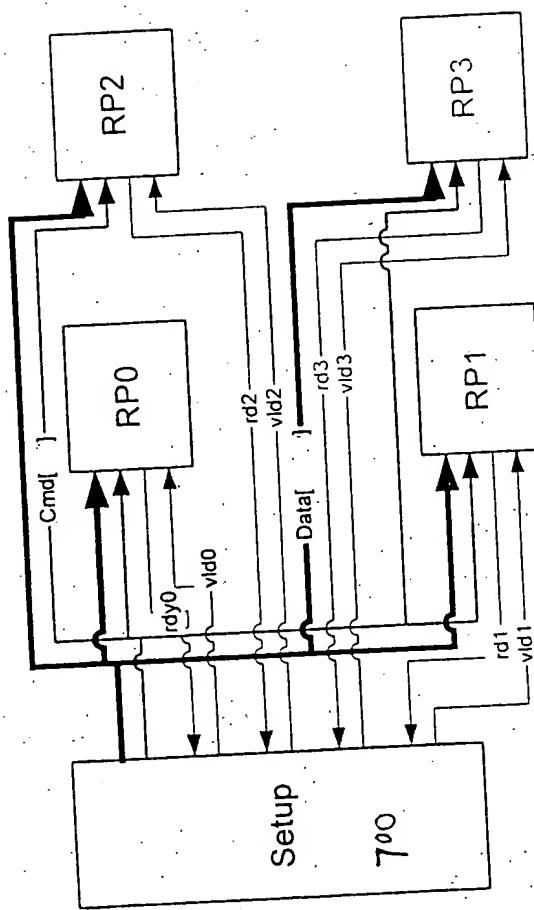


Figure 8A

Figure 8B

4 Pipe Config 0	
nxm pixels Tile 00 RP0	nxm pixels Tile 01 RP1
nxm pixels Tile 10 RP2	nxm pixels Tile 11 RP3

4 Pipe Config 1	
nxm pixels Tile 00 RP0	nxm pixels Tile 10 RP2
nxm pixels Tile 01 RP1	nxm pixels Tile 11 RP3

Figure 8C

2 Pipe Config 0

2 Pipe Config 0	
nxm pixels Tile 00 RP0	nxm pixels Tile 01 RP1
860	

2 Pipe Config 1	
nxm pixels Tile 00 RP0	nxm pixels Tile 01 RP1
861	

2 Pipe Config 2	
nxm pixels Tile 00 RP0	nxm pixels Tile 01 RP1
862	

Figure 8D

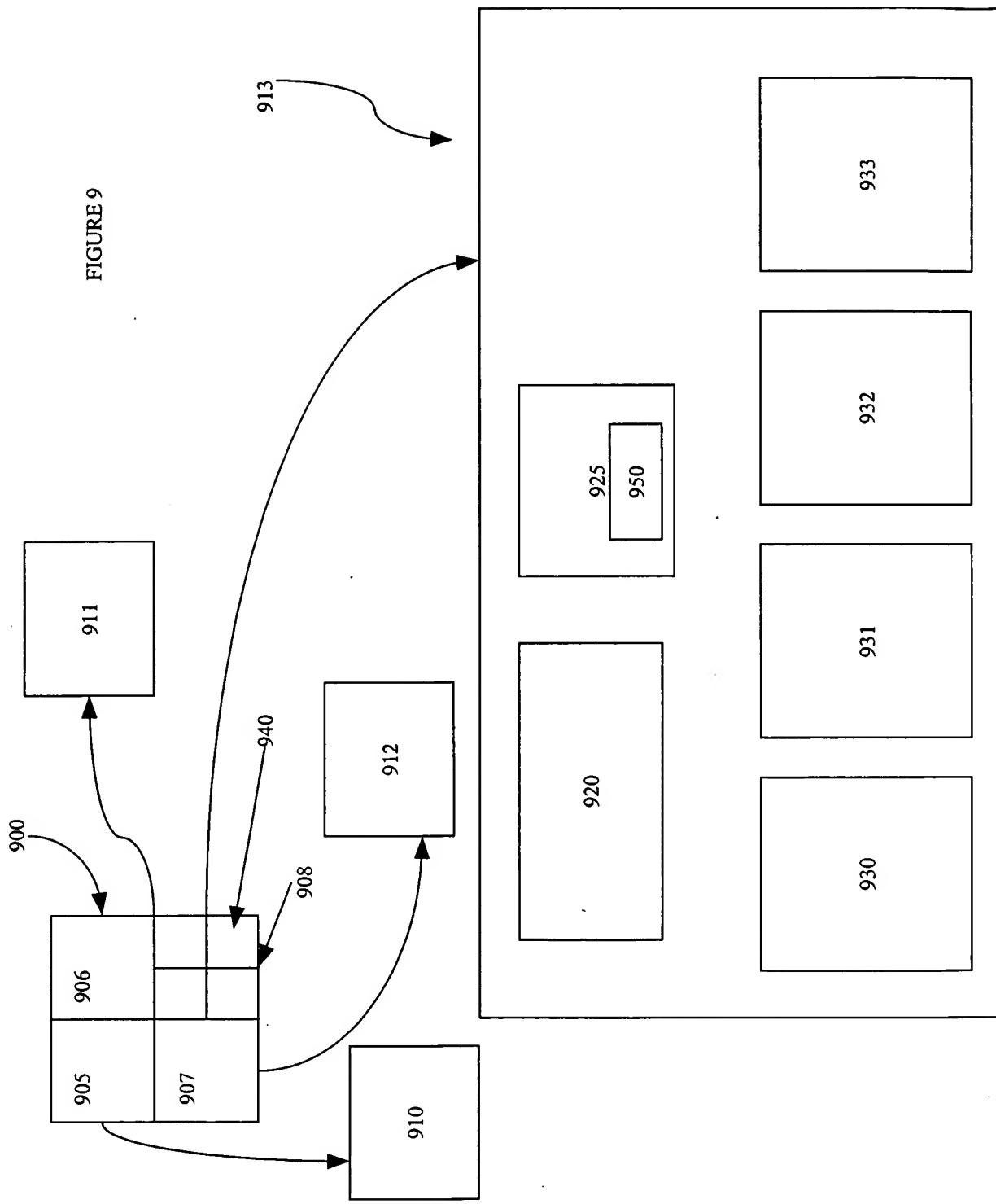
2 Pipe Config 3	
nxm pixels Tile 00 RP0	nxm pixels Tile 01 RP1
863	

2 Pipe Config 4	
nxm pixels Tile 00 RP0	nxm pixels Tile 01 RP1
864	

1 Pipe Config 0	
nxm pixels Tile 00 RP0	nxm pixels Tile 01 RP1
865	

Figure 8E

FIGURE 9



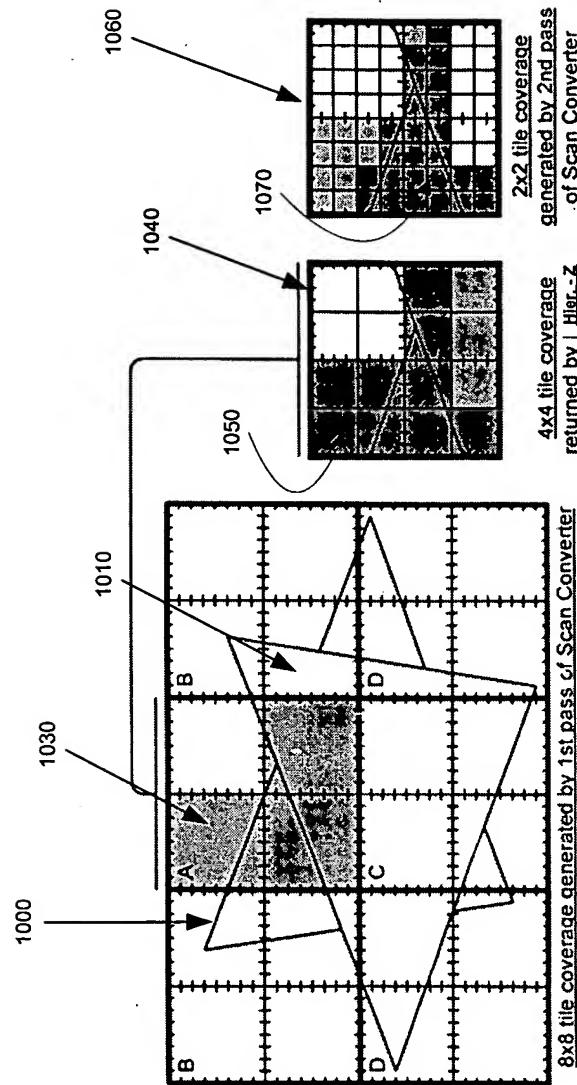


FIGURE 10A

FIGURE 10B

FIGURE 10C

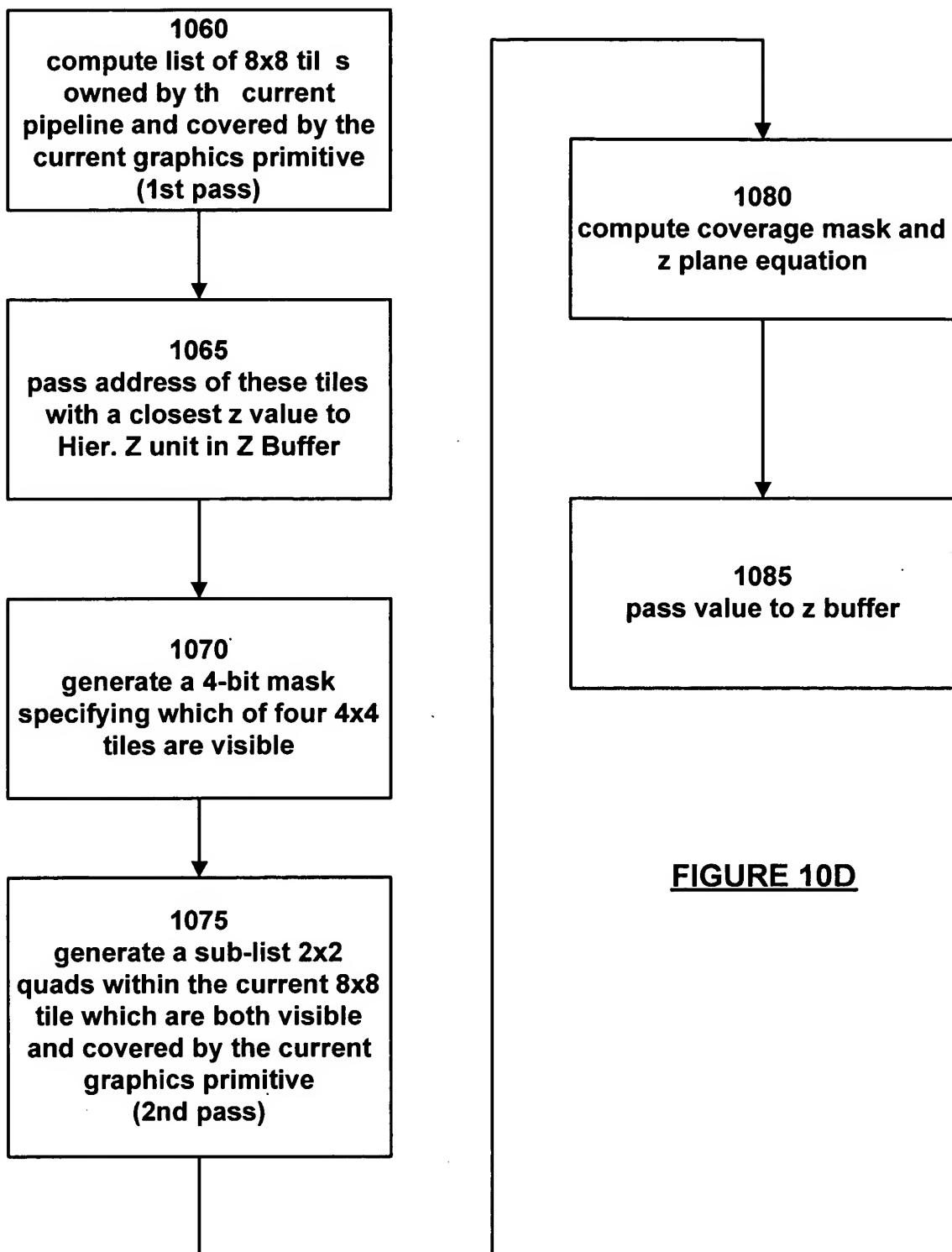


FIGURE 10D

